

(Shielding removed for clarity)

DP1203 - C868 / C915

868 and 915MHz Drop-In RF Transceiver Modules

Small Form Factor and Direct Digital Interface Modules

GENERAL DESCRIPTION

The DP1203 is a 30mm x 18mm shielded radio transceiver module available for use in either the 868-870MHz or the 902-928MHz ISM bands. The module is suitable for circuit applications which have to satisfy either European (ETSI EN300-220-1, EN301-489-3) or the North American (FCC part 15.247, 15.249) regulatory standards.

Virtually no RF knowledge is required to use this RF module. Wireless communication can be obtained with just the addition of a suitable antenna and a microcontroller. The DP1203 fulfils a wide range of application requirements, ranging from basic point-to-point communication to more complex multipoint process control functions.

The module is suitable for surface mount reflow assembly.

Semtech provides basic firmware routines for controlling the module and typical applications.

APPLICATIONS

- Home automation and access control
- · Process and building control
- Cable replacement

KEY PRODUCT FEATURES

- Instant RF solder and use!
- No RF knowledge required
- Small size 30mm x 18mm
- Direct Digital Interface
- Supply voltage 2.4V 3.6V
- Minimum frequency synthesizer step size of 500Hz
- Maximum output power of +11dBm
- Receiver sensitivity of -111dBm
- Very high data rate up to 153.2kbps
- Current consumption Tx = 62mA @ 11dBm
- Current consumption Rx=14mA

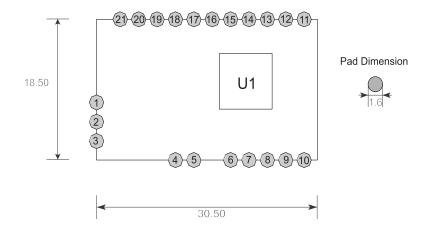
DEVICE OPTIONS

Part Number	Description	Packing Method	Pin- packa ge
DP1203C868	868-870MHz	Tape&Reel	Board
DP1203C915	902-928MHz	Tape&Reel	Board



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PIN	NAME	I/O	DESCRIPTION
1	GND	IN	Ground
2	RF	IN/OUT	RF input / output terminal
3	GND	IN	Ground
4	VDDP		VDD for power amplifier
5	VDDA		VDD for analog blocks
6	GND		Ground
7	VDD		Supply voltage
8	/EN	IN	3-wire interface communication enable signal
9	SWITCH	IN/OUT	Receiver or transmitter mode selection
10	GND	IN	Ground
11	GND	IN	Ground
12	SO	OUT	3-wire serial data interface: Data Out
13	SI	IN	3-wire serial data interface: Data In
14	SCK	IN	3-wire serial data interface: Data Clock
15	CLKOUT	OUT	Programmable reference clock output
16	DCLK	OUT	Receiver data clock
17	DATA	IN/OUT	Transmitter data input or receiver data output
18	DATAIN	IN	Transmitter data input
19	PATTERN	OUT	Receiver pattern recognition output
20	RX	IN	Antenna switch RX Select
21	TX	IN	Antenna switch TX select

1.1 ABSOLUTE MAXIMUM OPERATING RANGES

Description	Min	Max	Unit
Supply voltage	2.4	3.6	V
Operating temperature	-40	+85	°C
Storage temperature	-55	125	°C
Soldering temperature (max 15 sec)		260	°C



CAUTION: ESD sensitive device.

Precaution should be taken when handling the device in order to prevent permanent damage



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1.2 SPECIFICATIONS

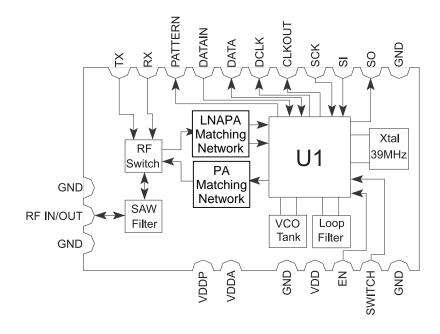
Tamb = 25° C; VDD = 3.3V, Frequency Deviation, Δ_f = 55 kHz, Bit-Rate, DR = 4.8kbps pseudo-random bit sequence unless otherwise specified.

IDD _{SL} IDD _{ST} IDD _{RX}	Synthesizer Frequency Range Sleep mode supply current Standby mode supply current RX mode supply current TX mode supply current RF Sensitivity	DP1203C868 DP1203C915 39 MHz running P _{RF} = 5 dBm P _{RF} = 11dBm	868 902	0.2 0.85 14 33 62	870 928 1 1.1 17 45 75	MHz MHz uA mA mA
IDD _{SL} IDD _{ST} IDD _{RX}	Sleep mode supply current Standby mode supply current RX mode supply current TX mode supply current	39 MHz running P _{RF} = 5 dBm	902	0.2 0.85 14 33	1 1.1 17 45	uA mA mA
IDD _{ST} IDD _{RX}	Standby mode supply current RX mode supply current TX mode supply current	P _{RF} = 5 dBm		0.85 14 33	1.1 17 45	mA mA
IDD _{ST} IDD _{RX}	Standby mode supply current RX mode supply current TX mode supply current	P _{RF} = 5 dBm		0.85 14 33	1.1 17 45	mA mA
IDD_{RX}	RX mode supply current TX mode supply current	P _{RF} = 5 dBm		14 33	17 45	mA
	TX mode supply current			33	45	
IDD _{TX}					-	mΑ
	RF Sensitivity	$P_{RF} = 11dBm$		62	75	
	RF Sensitivity				75	mA
	RF Sensitivity				1	
RFS	Tit Conolavity	DR = 4.8 kbps / BER = 0.1%		-111	-108	dBm
1		T			ı	1
	Frequency Deviation	Programmable	1	-	255	kHz
DR	Bit rate	Programmable	1.2	-	152.3	Kb/s
P_{RF}	RF output power	Programmable.				
		P _{RFMAX}	8	11	-	dBm
T. T.		T =				1
	Transmitter wake-up time	From oscillator enabled	-	200	250	us
	Receiver baseband wake up time	From oscillator enabled	-	1.5	1.8	ms
t _{XTAL}	Quartz oscillator wake up time		-	-	1	ms
		T =			1	
f _{XTAL}	Quartz oscillator frequency	Fundamental mode osc.		39		MHz
V _{IH}	Digital input level high	% VDD	75	_	l <u>-</u>	%
	Digital input level low	% VDD	-	_	25	%
	Digital output level high	% VDD	75	_	-	%
	Digital output level low	% VDD	-		25	%

The DP1203 is a cost effective high performance radio transceiver module designed for the wireless transmission of digital information over distances of >500 meters in free space.

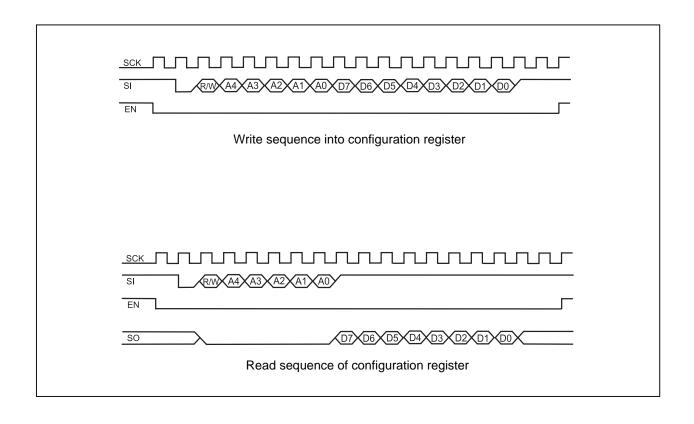
The module is based on the RF transceiver circuit from SEMTECH, the XE1203. For more information on the XE1203, please refer to the Semtech web site.

All high frequency circuits and the 39MHz reference crystal are enclosed inside a shielding can. The module incorporates an antenna switch driven by two external pins (RX and TX), and a SAW Filter placed between the antenna port (RF IN/OUT). The switch provides a superior margin to satisfy the European or the North American regulatory standards.



A 3-wire bi-directional bus (SCK, SI, SO) is used to control the DP1203. The output signal, SO, is provided by the DP1203 in opposition to the SCK and SI which need to be provided by the external application as an 8-bit microcontroller. An access *Read* or *Write* with the XE1203 is possible only when the enable signal is active (active LOW).

For more information about the 3-wire bus, please refer to the XE1203 datasheet chapter; *Interface definition, principles of operation.*





The DP1203 has 2 main operating modes (Mode 1, Mode 2); each mode is subdivided into 4 modes illustrated in the table below. The switching between Mode1 and Mode 2 can be done either through the 3-wire bus (Chip_config register) or by using the pin SWITCH. The selection depends on the Switch_ext which is a XE1203 register.

Switch_ext Bit 3, Address 00010	Chip_config Bit 0, Address 00000	SWITCH (pin)	Mode selected
0	0	Set in Output - "1" DP1203 is in transmitter - "0" DP1203 other modes	Mode 1 Bit 7-6, Address 00110 - 0 0 : sleep mode - 0 1 : standby mode - 1 0 : receiver mode - 1 1 : transmitter mode
0	1	Set in Output - "1" DP1203 is in transmitter - "0" DP1203 other modes	Mode 2 Bit 7-6, Address 01001 - 0 0 : sleep mode - 0 1 : standby mode - 1 0 : receiver mode - 1 1 : transmitter mode
1	X	0	Mode 1 Bit 7-6, Address 00110 - 0 0 : sleep mode - 0 1 : standby mode - 1 0 : receiver mode - 1 1 : transmitter mode
1	X	1	Mode 2 Bit 7-6, Address 01001 - 0 0 : sleep mode - 0 1 : standby mode - 1 0 : receiver mode - 1 1 : transmitter mode

For more information about the modes of operation, please refer to the XE1203 Datasheet on the Semtech website.

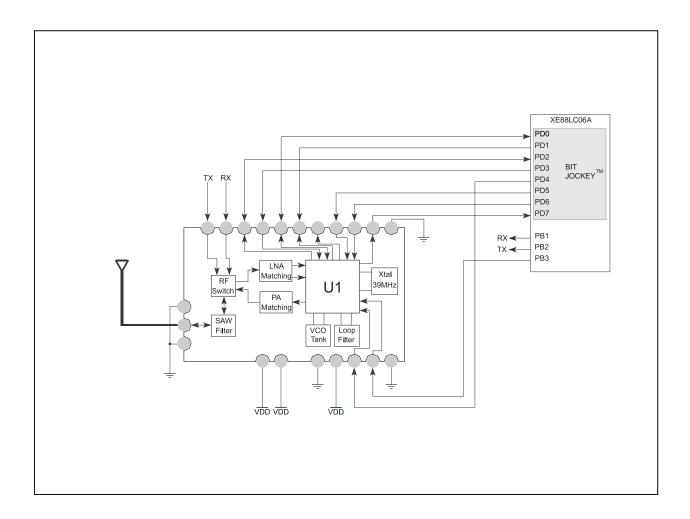


1.3 STANDARD SEQUENCE FOR SWITCHING BETWEEN RECEIVER AND TRANSMITTER

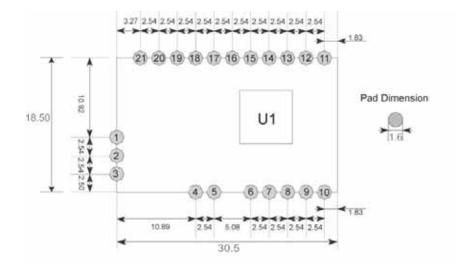
The drop-in module DP1203 is able to switch between any configuration by using the 3-wire bus or by using the pin SWITCH. This section describes the switching sequence from Mode 1 to Mode 2.

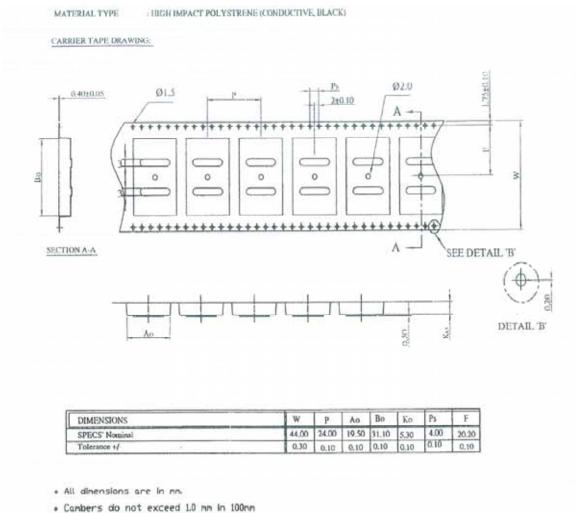
	ddress 00010)			
sck				
SI	A4\A3\A2\A1\A0\D7=1	A4\A3\A2\A1\A0\D7=1		
EN			_	
Mode	Mode 1 : Transmitter	Mode 2: Receiver	_	
Pad SWITCH as output			_	
	! OW/TO!!			
ching sequence by	pad SWITCH			
	ddress 00010)			
ch_ext = 1 (Bit 3, Ad				
ch_ext = 1 (Bit 3, Ad			_	
Pad SWITCH				
ch_ext = 1 (Bit 3, Ad Pad SWITCH as input				

The schematic below shows the DP1203 interfaced with a Semetch's microcontroller XE8806A. In this typical application, the pad SWITCH is used as an input and the two signals TX and RX are controlled directly by the microcontroller.



The following diagram shows the physical footprint and dimensions of the DP1203 drop-in module, which should be implemented on the mother board.





- . The naximum cumulative talerance is +/-0.2 for 10 sprocket hale pitch
- * The thickness of carrier tape is 0.4mm



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