



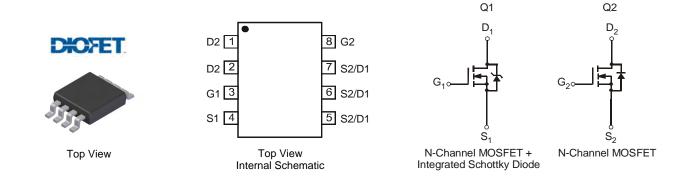
ASYMMETRIC DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- DIOFET utilize a unique patented process to monolithically integrate a MOSFET and a Schottky in a single die to deliver:
 - Low R_{DS(on)} minimizes conduction loss
 - Low V_{SD} reducing the losses due to body diode construction
 - Low Q_{rr} lower Q_{rr} of the integrated Schottky reduces body diode switching losses
 - Low gate capacitance (Q_g/Q_{gs}) ratio reduces risk of shootthrough or cross conduction currents at high frequencies
 - Avalanche rugged I_{AR} and E_{AR} rated
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.072 grams (approximate)



Ordering Information (Note 3)

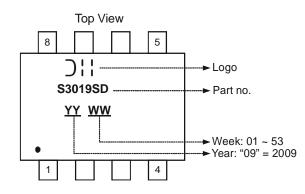
Part Number	Case	Packaging
DMS3019SSD-13	SO-8	2500 / Tape & Reel

Notes: 1. No purposefully added lead.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

3. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information





Maximum Ratings - Q1 @TA = 25°C unless otherwise specified

Characteri	Symbol	Value 30	Unit		
Drain-Source Voltage	V _{DSS}		V		
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 4) V _{GS} = 10V	Steady State	T _A = 25°C T _A = 70°C	I _D	7.0 5.6	A
Continuous Drain Current (Note 5) V_{GS} = 10V	Steady State	T _A = 25°C T _A = 70°C	I _D	9.0 7.0	A
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	T _A = 25°C T _A = 70°C	ID	8.0 6.5	A
Pulsed Drain Current (Note 6)			I _{DM}	40	A
Avalanche Current (Notes 6 & 7)			I _{AR}	13	А
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.3mH		E _{AR}	25.4	mJ	

Maximum Ratings - Q2 @TA = 25°C unless otherwise specified

Characteri	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 4) V _{GS} = 10V	Steady State	T _A = 25°C T _A = 70°C	I _D	5.7 4.6	А
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	T _A = 25°C T _A = 70°C	ID	7.0 5.6	А
Continuous Drain Current (Note 5) $V_{GS} = 4.5V$	Steady State	T _A = 25°C T _A = 70°C	ID	6.0 4.7	А
Pulsed Drain Current (Note 6)			ID	40	A
Avalanche Current (Notes 6 & 7)			I _{AR}	16	A
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.1mH		E _{AR}	12.8	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	PD	1.19	W
Thermal Resistance, Junction to Ambient $@T_A = 25^{\circ}C$ (Note 4)	R _{θJA}	107	°C/W
Power Dissipation (Note 5)	PD	1.79	W
Thermal Resistance, Junction to Ambient $@T_A = 25^{\circ}C$ (Note 5)	R _{θJA}	70	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 4. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout. The value in any given application depends on the user's specific board design. Device contains two active die running at equal power.
5. Device mounted on 1 inch x 1 inch FR4 PCB with high coverage of single sided 1oz copper, in still air conditions. Device contains two active die running

at equal power.

6. Repetitive rating, pulse width limited by junction temperature.

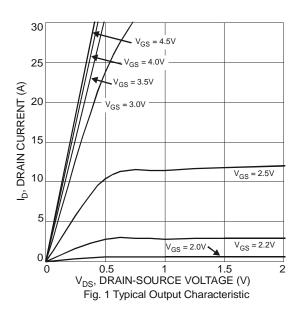
7. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep $T_J = 25^{\circ}C$

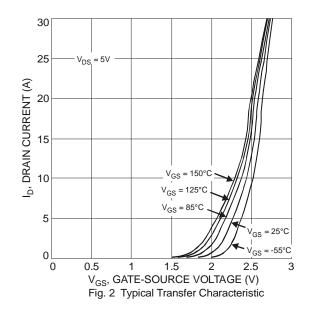


Electrical Characteristics – Q1	@ $T_A = 25^{\circ}C$ unless otherwise stated

Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						·
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current	I _{DSS}	-	-	0.1	mA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	-	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)				-		-
Gate Threshold Voltage	V _{GS(th)}	1.0	-	2.4	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance	P		10	15	mΩ	$V_{GS} = 10V, I_D = 9A$
Static Drain-Source On-Resistance	R _{DS (ON)}	-	12	18	111.5.2	$V_{GS} = 4.5V, I_D = 7A$
Forward Transfer Admittance	Y _{fs}	-	5	-	S	$V_{DS} = 5V, I_{D} = 9A$
Diode Forward Voltage	V _{SD}	-	0.4	1	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	-	1932	-		$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Output Capacitance	Coss	-	154	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	121	-		
Gate Resistance	Rg	-	2.7	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	18.1	-		V _{DS} = 15V, V _{GS} = 4.5V, I _D = 9A
Total Gate Charge (V _{GS} = 10V)	Qg	-	42.0	-	nC	$V_{DS} = 15V, V_{GS} = 10V, I_D = 9A$
Gate-Source Charge	Q _{gs}	-	4.5	-	nc	
Gate-Drain Charge	Q _{gd}	-	4.0	-		
Turn-On Delay Time	t _{D(on)}	-	6.16	-		
Turn-On Rise Time	tr	-	7.22	-		V _{GS} = 10V, V _{DS} = 15V,
Turn-Off Delay Time	t _{D(off)}	-	36.76	-	ns	$R_G = 3\Omega, R_L = 1.7\Omega$
Turn-Off Fall Time	t _f	-	5.38	-	1	

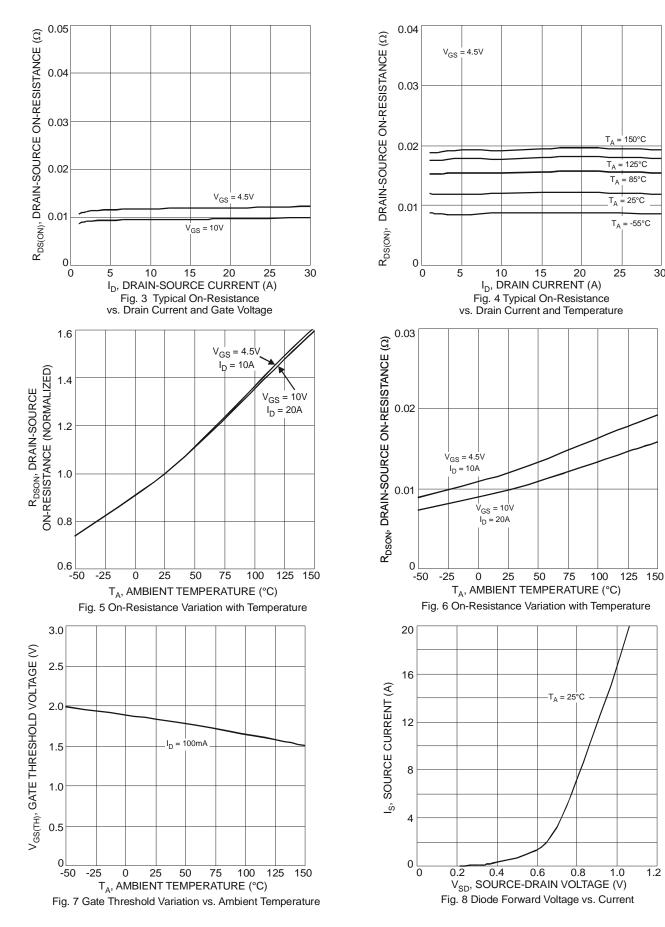
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing. Notes:







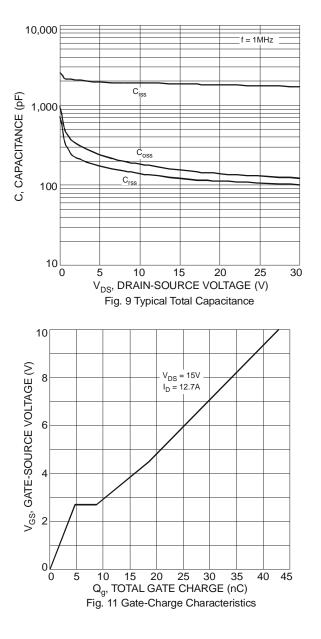
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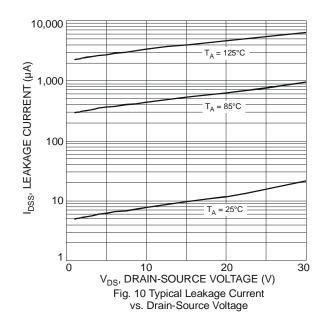


1.2



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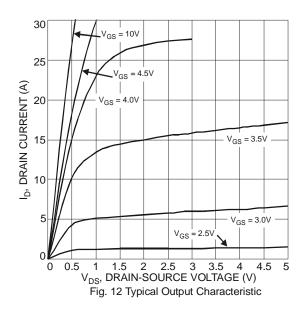


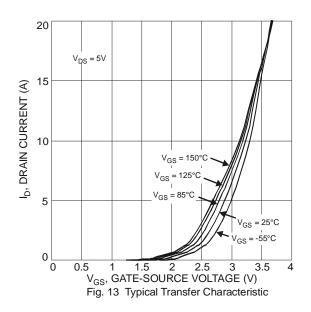


Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	1.0	-	2.4	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance	P		15	23	mΩ	$V_{GS} = 10V, I_D = 10A$
	R _{DS (ON)}	-	25	33	111.5.2	$V_{GS} = 4.5 V, I_D = 7.5 A$
Forward Transfer Admittance	Y _{fs}	-	2.5	-	S	$V_{DS} = 5V, I_{D} = 10A$
Diode Forward Voltage	V _{SD}	-	0.65	1.0	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	-	478.9	-		$\label{eq:VDS} \begin{split} V_{DS} &= 15V, V_{GS} = 0V, \\ f &= 1.0MHz \end{split}$
Output Capacitance	Coss	-	96.7	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	61.4	-		
Gate Resistance	Rg	0.4	1.1	1.6	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	5.0	-		$V_{DS} = 15V, V_{GS} = 4.5V, I_D = 10A$
Total Gate Charge (V _{GS} = 10V)	Qg	-	10.5	-	nC	$V_{DS} = 15V, V_{GS} = 10V, I_D = 10A$
Gate-Source Charge	Q _{gs}	-	1.8	-	nc	
Gate-Drain Charge	Q _{gd}	-	1.6	-		
Turn-On Delay Time	t _{D(on)}	-	2.9	-		
Turn-On Rise Time	tr	-	7.9	-		$V_{GS} = 10V, V_{DS} = 15V,$
Turn-Off Delay Time	t _{D(off)}	-	14.6	-	ns	$R_G = 3\Omega, R_L = 1.5\Omega$
Turn-Off Fall Time	t _f	-	3.1	-]	

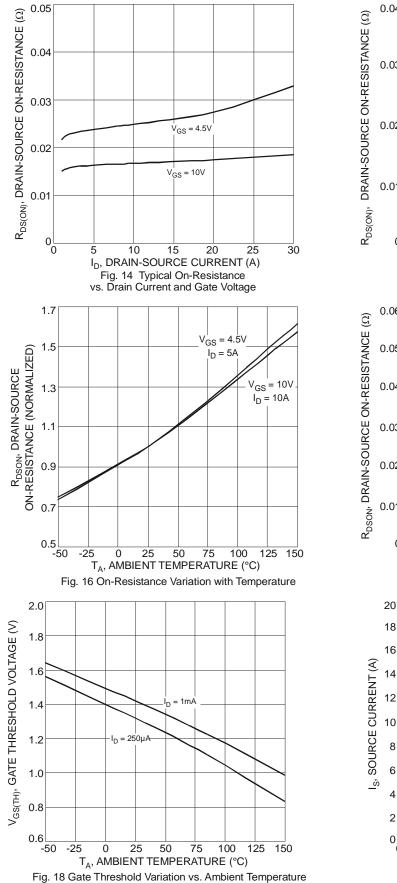
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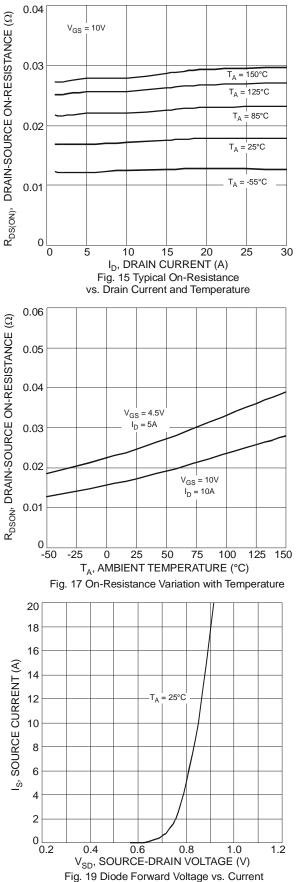
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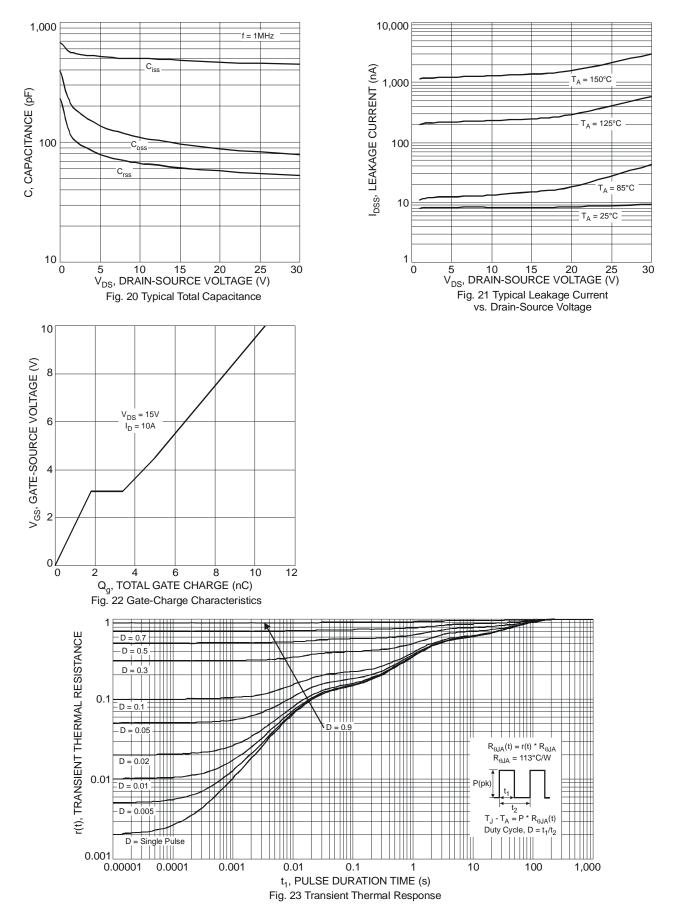






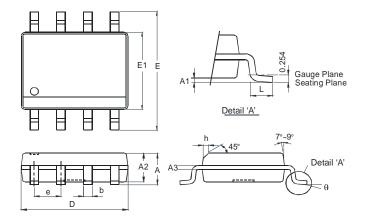


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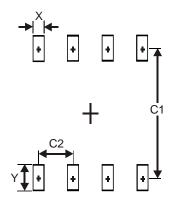


Package Outline Dimensions



SO-8					
Dim	Min	Max			
Α	-	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
E	5.90	6.10			
E1	3.85	3.95			
е	1.27	Тур			
h	-	0.35			
L	0.62	0.82			
θ	0°	8°			
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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