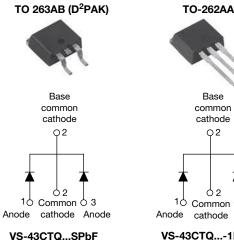
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**Vishay Semiconductors** 

# High Performance Schottky Rectifier, 2 x 20 A



Base common cathode 02 02 Common 👌 3 cathode Anode

VS-43CTQ...-1PbF

PRODUCT SUMMARY					
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA				
I <sub>F(AV)</sub>	2 x 20 A				
V <sub>R</sub>	80 V, 100 V				
V <sub>F</sub> at I <sub>F</sub>	0.67 V				
I <sub>RM</sub> max.	11 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	7.50 mJ				

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Center tap configuration
- · Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- RoHS COMPLIANT HALOGEN FREE

- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	40	А		
V <sub>RRM</sub>		80/100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	A		
V <sub>F</sub>	20 $A_{pk}$ , $T_J$ = 125 °C (per leg)	0.67	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-43CTQ080SPbF VS-43CTQ080-1PbF	VS-43CTQ100SPbF VS-43CTQ100-1PbF	UNITS
Maximum DC reverse voltage	V <sub>R</sub>	80	100	V
Maximum working peak reverse voltage	V <sub>RWM</sub>	80	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS
Maximum average per leg				20	
forward current per device	device $I_{F(AV)}$ 50 % duty cycle at T <sub>C</sub> = 135 °C, rectangular waveform		C, rectangular waveform	40	
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	rated load 850 A	
surge current per leg See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	275	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_{J} = 25 \ ^{\circ}C, I_{AS} = 0.50 \ A, L = 60$	) mH	7.50	mJ
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by T <sub>J</sub> maxim	•	0.50	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		20 A	T.I = 25 °C	0.81	
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j=25 0	0.98	V
See fig. 1	VFM V	20 A	T.I = 125 °C	0.67	
		40 A	1j=125 0	0.81	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1 mA	
See fig. 2	'RM \''	T <sub>J</sub> = 125 °C	VR - naleu VR	11	III/A
Threshold voltage	V <sub>F(TO)</sub>			0.71	V
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		0.43	mΩ
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range	ge 100 kHz to 1 MHz), 25 °C	1480	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	nm from package body	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

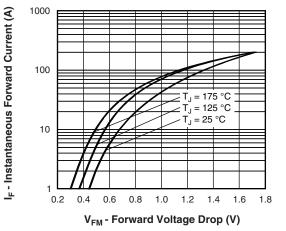
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

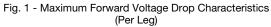
THERMAL - MECHAN	ICAL SP	ECIFICAT	IONS				
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	e	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C		
Maximum thermal resistance, junction to case per leg		D		2.0			
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.0	°C/W		
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50	1		
Approvimeto weight				2	g		
Approximate weight				0.07	oz.		
Mounting torque	minimum			6 (5)	kgf ⋅ cm		
Mounting torque	maximum			12 (10)	(lbf · in)		
						43CT0	2080S
			Case style TO-263AB (D <sup>2</sup> PAK)		Q100S		
Marking device	Marking device			43CTC	080-1		
			Case style TO-262AA		43CTQ100-1		

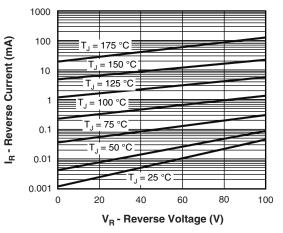


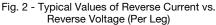
## VS-43CTQ...SPbF, VS-43CTQ...-1PbF Series

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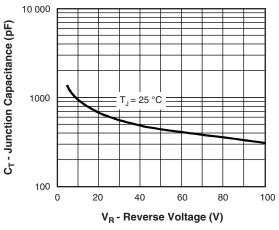


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

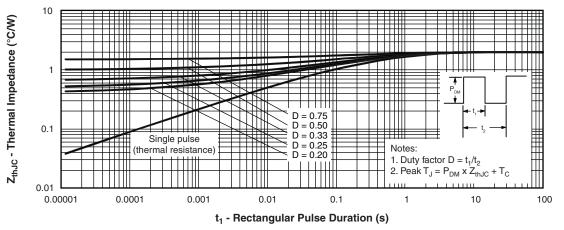


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

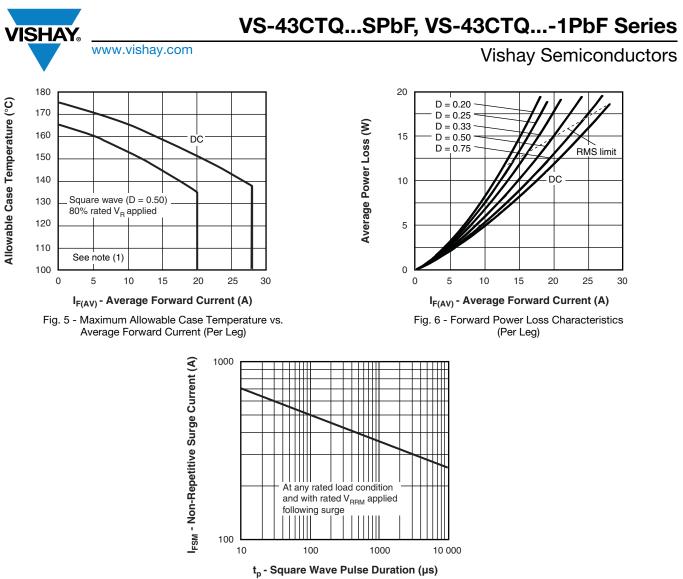


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

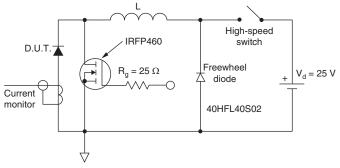


Fig. 8 - Unclamped Inductive Test Circuit

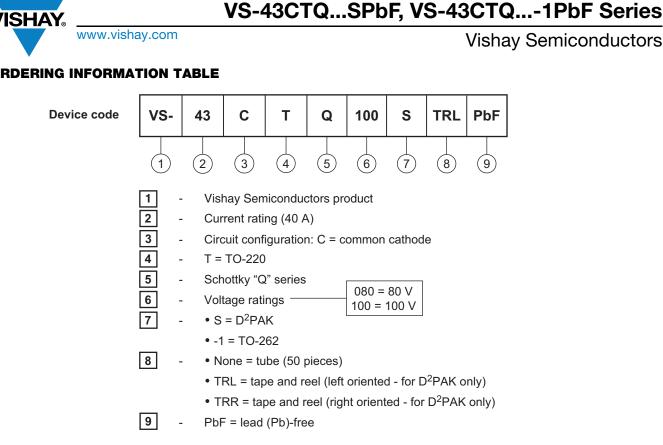
#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ at \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (1 \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ at \ \mathsf{V}_{\mathsf{R1}} = \mathsf{10} \ \mathsf{V} \end{array}$

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LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95014			
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			
SPICE model	www.vishay.com/doc?95065			

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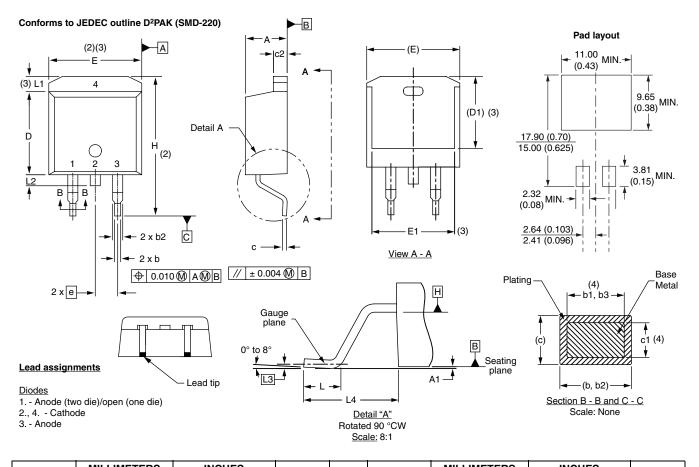
### **ORDERING INFORMATION TABLE**

Vishay High Power Products

# D<sup>2</sup>PAK, TO-262

### DIMENSIONS FOR D<sup>2</sup>PAK in millimeters and inches

SHA



SYMBOL	MILLIM	ETERS	INC	HES	NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
с	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

<sup>(7)</sup> Outline conforms to JEDEC outline TO-263AB

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$  Thermal pad contour optional within dimension E, L1, D1 and E1
- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only
- <sup>(5)</sup> Datum A and B to be determined at datum plane H
- <sup>(6)</sup> Controlling dimension: inch

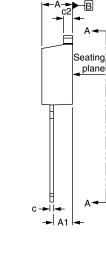
**Vishay High Power Products** 

D<sup>2</sup>PAK, TO-262



### DIMENSIONS FOR TO-262 in millimeters and inches







Lead tip



SYMBOL -	MILLIM	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100	BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- <sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Controlling dimension: inches

<sup>(6)</sup> Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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